

	Hits	Search Text	Time Stamp
1	22046	(SERIAL\$2 NEAR6 (TRANSMISSION OR TRANSMIT\$4 OR TRANSFER\$3))	2000/03/10 15:26
2	18623	((FIFO OR ((ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY)))	2000/03/10 14:18
3	68172	((FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED))	2000/03/10 15:43
4	10	((SERIAL\$2 NEAR6 (TRANSMISSION OR TRANSMIT\$4 OR TRANSFER\$3))) SAME (((FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED))) SAME ((FIFO OR ((ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))))	2000/03/10 15:05
5	1749	((FIFO OR ((ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME (SENDER OR TRANSCIEVER OR TRANSMITTER OR RECEIVER))	2000/03/10 15:15
6	318	((((FIFO OR ((ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME (SENDER OR TRANSCIEVER OR TRANSMITTER OR RECEIVER))) AND (((FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED))))	2000/03/10 15:15
7	165	((((FIFO OR ((ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME (SENDER OR TRANSCIEVER OR TRANSMITTER OR RECEIVER))) SAME ((SERIAL\$2 NEAR6 (TRANSMISSION OR TRANSMIT\$4 OR TRANSFER\$3))))	2000/03/10 15:15
8	22	(((((FIFO OR ((ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME (SENDER OR TRANSCIEVER OR TRANSMITTER OR RECEIVER))) SAME ((SERIAL\$2 NEAR6 (TRANSMISSION OR TRANSMIT\$4 OR TRANSFER\$3)))) AND (((FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED))))	2000/03/10 15:15
9	495	((FIFO OR ((ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME TRANSMITTER SAME RECEIVER)	2000/03/10 15:27
10	111	(((((FIFO OR ((ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME TRANSMITTER SAME RECEIVER)) AND (((FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED))))	2000/03/10 15:27
11	6	(((((FIFO OR ((ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME TRANSMITTER SAME RECEIVER)) same (((FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED))))	2000/03/10 15:27

	Hits	Search Text	Time Stamp
12	71	(((((FIFO OR (ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME TRANSMITTER SAME RECEIVER)) AND (((FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED)))) AND ((SERIAL\$2 NEAR6 (TRANSMISSION OR TRANSMIT\$4 OR TRANSFER\$3))))	2000/03/10 15:30
13	215	709/213.ccls.	2000/03/10 15:31
14	49	((709/213.CCLS.) AND ((FIFO OR (ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))))	2000/03/10 15:50
15	69554	((ASYNCHRONOUS OR FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED))	2000/03/10 15:50
16	3830	((FIFO OR (ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME (PACKET OR FRAME))	2000/03/10 15:51
17	701	(((((FIFO OR (ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME (PACKET OR FRAME))) AND ((ASYNCHRONOUS OR FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED))))	2000/03/10 15:51
18	66	(((((FIFO OR (ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME (PACKET OR FRAME))) same ((ASYNCHRONOUS OR FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED))))	2000/03/10 15:59
19	147174	(MODE NEAR2 OPERATI\$2)	2000/03/10 16:02
20	679	((FIFO OR (ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME ((MODE NEAR2 OPERATI\$2))	2000/03/10 16:03
21	25253	((MODE NEAR2 OPERATI\$2)) NEAR8 (SETT\$3 OR SET\$1 OR SELECT\$4))	2000/03/10 16:05
22	111	(((((MODE NEAR2 OPERATI\$2)) NEAR8 (SETT\$3 OR SET\$1 OR SELECT\$4))) SAME ((FIFO OR (ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))))	2000/03/10 16:05
23	76	(((((MODE NEAR2 OPERATI\$2)) NEAR8 (SETT\$3 OR SET\$1 OR SELECT\$4))) SAME ((FIFO OR (ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY)))) AND SERIAL\$2)	2000/03/10 16:11

	Hits	Search Text	Time Stamp
24	4894	((FIFO OR (ELASTIC OR CIRCULAR OR RING) NEAR2 (BUFFER OR MEMORY))) SAME (EMPTY OR FILL OR THRESHOLD OR FULL OR LIMIT))	2000/03/10 16:12
25	211	24.ab.	2000/03/10 16:12
26	50	((24.AB.) AND ((ASYNCHRONOUS OR FASTER OR DIFFERENT) NEAR4 (RATE OR SPEED)))	2000/03/10 16:22
27	36	((24.AB.) SAME (PACKET\$1 OR FRAME\$1))	2000/03/10 16:26
28	24640	(REGISTER\$1 NEAR2 (DESTINATION OR NODE OR TARGET OR CONFIGURATION OR CONTROL))	2000/03/10 16:32
29	42	((24.AB.) AND ((REGISTER\$1 NEAR2 (DESTINATION OR NODE OR TARGET OR CONFIGURATION OR CONTROL)))	2000/03/10 16:37
30	65	(elastic adj2 buffer\$1).ab.	2000/03/10 16:37

	Document ID	Issue Date	Title	Current OR
1	US 6012110 A	20000104	Apparatus and method for input data loss prevention with a buffered UART	710/60
2	US 6002733 A	19991214	Universal asynchronous receiver and transmitter	375/377
3	US 5987244 A	19991116	Power management masked clock circuitry, systems and methods	713/500
4	US 5949799 A	19990907	Minimum-latency data mover with auto-segmentation and reassembly	714/749
5	US 5943507 A	19990824	Interrupt routing circuits, systems and methods	710/48
6	US 5937202 A	19990810	High-speed, parallel, processor architecture for front-end electronics, based on a single type of ASIC, and method use thereof	712/19
7	US 5875312 A	19990223	Structure and method of performing DMA transfers between memory and I/O devices utilizing a single DMA controller within a notebook and docking	395/309
8	US 5872983 A	19990216	station computer system Power management interface system for use with an electronic wiring board article of manufacture	395/750.01

	Document ID	Issue Date	Title	Current OR
9	US 5870617 A	19990209	Systems, circuits and methods for mixed voltages and programmable voltage rails on integrated circuits	395/750.06
10	US 5870621 A	19990209	Quadrilateral multichip computer systems and printed circuit boards therefor	712/32
11	US 5867717 A	19990202	Dynamic system clocking and address decode circuits, methods and systems	713/323
12	US 5864702 A	19990126	Computer system power management interconnection circuitry, systems and methods	713/320
13	US 5862180 A	19990119	Differential encoding of self-clocking data streams	375/244
14	US 5859893 A	19990112	X-ray collimation assembly	378/154
15	US 5852370 A	19981222	Integrated circuits for low power dissipation in signaling between different-voltage on chip regions	326/81

	Document ID	Issue Date	Title	Current OR
16	US 5850556 A	19981215	Interruptible state machine	710/260
17	US 5848253 A	19981208	Computer system and an electronic circuit utilizing a single DMA controller and additional communication circuit to manage DMA transfers between memory and I/O devices	710/129
18	US 5845132 A	19981201	Computer system power management interconnection circuitry, system and methods	710/260
19	US 5842005 A	19981124	Clock control circuits, systems and methods	713/500
20	US 5835561 A	19981110	Scanning beam x-ray imaging system	378/98
21	US 5835733 A	19981110	Method and apparatus for implementing a single DMA controller to perform DMA operations for devices on multiple buses in docking stations, notebook and desktop computer system fast at-bus on chip circuits systems and methods	710/101
22	US 5822550 A	19981013	Computer system fast at-bus on chip circuits systems and methods	710/126

	Document ID	Issue Date	Title	Current OR
23	US 5809466 A	19980915	Audio processing chip with external serial port	704/258
24	US 5786921 A	19980728	Infrared communication device for multistandard operations	359/152
25	US 5784291 A	19980721	CPU, memory controller, bus bridge integrated circuits, layout structures, system and methods	716/10
26	US 5781780 A	19980714	Power management supply interface circuitry, systems and methods	713/300
27	US 5771373 A	19980623	Power management masked clock circuitry, systems and methods	713/500
28	US 5754837 A	19980519	Clock control circuits, systems and methods	713/500
29	US 5754764 A	19980519	Combination of input output circuitry and local area network systems	709/200
30	US 5754436 A	19980519	Adaptive power management processes, circuits and systems	713/300
31	US 5751785 A	19980512	Image reconstruction methods	378/146

	Document ID	Issue Date	Title	Current OR
32	US 5742695 A	19980421	Wavetable audio synthesizer with waveform volume control for eliminating zipper noise	381/104
33	US 5740176 A	19980414	Scalable multimedia network	370/440
34	US 5734919 A	19980331	Systems, circuits and methods for mixed voltages and programmable voltage rails on integrated circuits	1/1
35	US 5729720 A	19980317	Power management masked clock circuitry, systems and methods	713/500
36	US 5729584 A	19980317	Scanning-beam X-ray imaging system	378/146
37	US 5727221 A	19980310	Computer system power management interconnection circuitry and systems	713/310
38	US 5721933 A	19980224	Power management supply interface circuitry, systems and methods	713/300
39	US 5712848 A	19980127	Time division multiplex communications system including means for reducing undesired echoes resulting from circuit delays	370/280
40	US 5710911 A	19980120	Clock control circuits, systems and methods	713/500

	Document ID	Issue Date	Title	Current OR
41	US 5694432 A	19971202	Transmitter for a digital communication system which eliminates cumulative jitter	375/295
42	US 5689531 A	19971118	Receiver for a digital communication system which eliminates cumulative jitter	375/316
43	US 5684997 A	19971104	Integrated circuit design for handling of system management interrupts (SMI)	710/260
44	US 5680415 A	19971021	Repeater for a digital communication system which eliminates cumulative jitter	375/211
45	US 5680416 A	19971021	Digital communication system which eliminates cumulative jitter	375/219
46	US 5675808 A	19971007	Power control of circuit modules within an integrated circuit	713/322
47	US 5670732 A	19970923	Midi data transmitter, receiver, transmitter/receiver, and midi data processor, including control blocks for various operating conditions	84/645

	Document ID	Issue Date	Title	Current OR
48	US 5668338 A	19970916	Wavetable audio synthesizer with low frequency oscillators for tremolo and vibrato effects	84/629
49	US 5659466 A	19970819	Monolithic PC audio circuit with enhanced digital wavetable audio synthesizer	700/94
50	US 5651047 A	19970722	Maneuverable and locateable catheters	378/98.8
51	US 5644612 A	19970701	Image reconstruction methods	378/98.2
52	US 5627858 A	19970506	Multi-channel autobauding circuit	375/225
53	US 5617236 A	19970401	Infrared communication device for multistandard operations	359/172
54	US 5610967 A	19970311	X-ray grid assembly	378/154
55	US 5586123 A	19961217	Interface and loopback circuit for character based computer peripheral devices	714/716
56	US 5438590 A	19950801	Transmitting and receiving apparatus and method including punctured convolutional encoding and decoding	375/259

	Document ID	Issue Date	Title	Current OR
57	US 5153884 A		Intelligent network interface circuit	714/748
58	US 4965794 A	19901023	Telecommunications FIFO	370/506
59	US 4839893 A	19890613	Telecommunications FIFO	370/506
60	US 4751742 A	19880614	Priority coding of transform coefficients	382/240
61	US 4739321 A	19880419	Decentralized line reservation interface within a local data network	340/825.5
62	US 4481622 A	19841106	High speed dial-up telephone circuit full duplex data transmission	370/286
63	US 4382160 A	19830503	techniques Methods and apparatus for encoding and constructing signals	704/211
64	US 4356546 A	19821026	Fault-tolerant multi-computer system	714/10

	Document ID	Issue Date	Title	Current OR
65	US 4342083 A		Communication system for a multiple-computer system	709/253
66	US 4333144 A	19820601	Task communicator for multiple computer system	709/102

	Document ID	Issue Date	Title	Current OR
67	US 4330826 A		Synchronizer and synchronization system for a multiple computer system	714/12
68	US 4323966 A	19820406	Operations controller for a fault-tolerant multiple computer system	714/1

	Document ID	Issue Date	Title	Current OR
69	US 4321666 A		Fault handler for a multiple computer system	714/10
70	US 4318173 A		Scheduler for a multiple computer system	709/103

	Document ID	Issue Date	Title	Current OR
71	US 4187394 A	19800205	High-speed data link for moderate distances and noisy environments	370/282